

U.S. UTILITY Patent Application

SCANNED <i>NT</i>	O.I.P.E. <i>Q.A.</i>	PATENT DATE
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APPLICATION NO. 09/966914	CONT/PRIOR	CLASS 257	SUBCLASS 686	ART UNIT 2841 282	EXAMINER Williams Fitzma
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APPLICANTS Sehat Sutardja

TITLE Integrated chip package having intermediate substrate

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No.	(Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal months of this patent have been disclaimed.	(Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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